



(12) **EUROPEAN PATENT APPLICATION**

(43) Date of publication:
04.10.2001 Bulletin 2001/40

(51) Int Cl.7: **H04L 27/26, H04L 25/03**

(21) Application number: **01102040.1**

(22) Date of filing: **30.01.2001**

(84) Designated Contracting States:
AT BE CH CY DE DK ES FI FR GB GR IE IT LI LU
MC NL PT SE TR
 Designated Extension States:
AL LT LV MK RO SI

(72) Inventors:
 • Belotserkovsky, Maxim B.,
 c/o Thomson Multimedia
 92100 Boulogne (FR)
 • Litwin, Louis Robert Jr, c/o Thomson Multimedia
 92100 Boulogne (FR)

(30) Priority: **22.02.2000 US 511185**

(71) Applicant: **THOMSON Licensing**
92100 Boulogne (FR)

(74) Representative: **Ruellan-Lemonnier, Brigitte**
THOMSON multimedia,
Licensing and Intellectual Property,
46 Quai Alphonse Le Gallo
92100 Boulogne Billancourt (FR)

(54) **Reduced complexity FFT window synchronization for an orthogonal frequency division multiplexing system**

(57) An Orthogonal Frequency Division Multiplexing (OFDM) receiver that compensates for FFT window drift by extracting pilots (62) from a fast Fourier transformed (56) and equalized (58) OFDM signal, and processing the extracted pilots to derive an FFT window adjustment

factor (65) and an associated equalizer tap adjustment value (67). The OFDM receiver simultaneously controls the position of an FFT window and the phase of equalizer taps using the derived FFT adjustment factor and equalizer tap adjustment value.

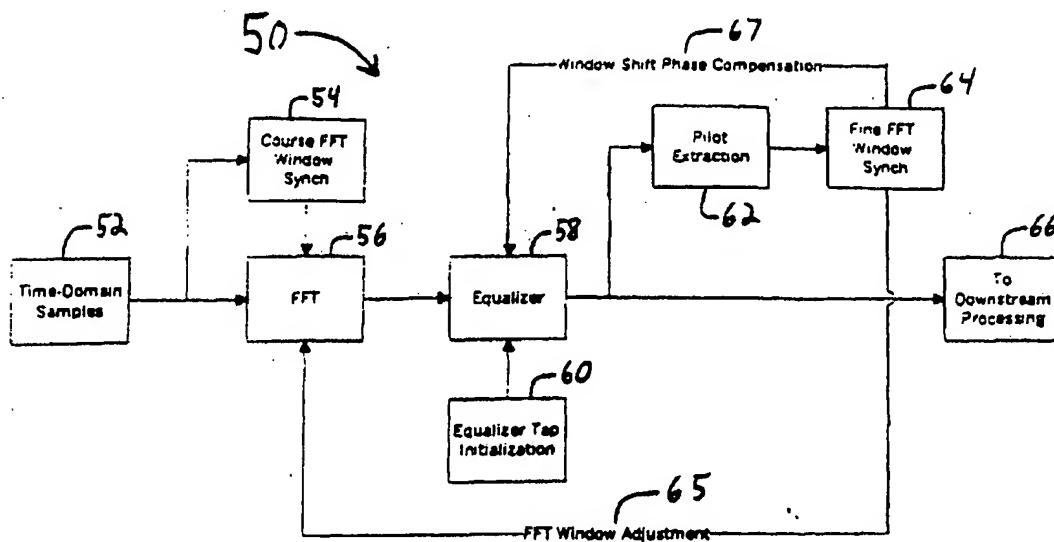


FIG. 5

Description

[0001] The present invention relates to processing orthogonal frequency division multiplexed (OFDM) signals.

[0002] A wireless LAN (WLAN) is a flexible data communications system implemented as an extension to, or as an alternative for, a wired LAN within a building or campus. Using electromagnetic waves, WLANs transmit and receive data over the air, minimizing the need for wired connections. Thus, WLANs combine data connectivity with user mobility, and, through simplified configuration, enable movable LANs. Some industries that have benefited from the productivity gains of using portable terminals (e.g., notebook computers) to transmit and receive real-time information are the digital home networking, health-care, retail, manufacturing, and warehousing industries.

[0003] Manufacturers of WLANs have a range of transmission technologies to choose from when designing a WLAN. Some exemplary technologies are multi-carrier systems, spread spectrum systems, narrowband systems, and infrared systems. Although each system has its own benefits and detriments, one particular type of multicarrier transmission system, orthogonal frequency division multiplexing (OFDM), has proven to be exceptionally useful for WLAN communications.

[0004] OFDM is a robust technique for efficiently transmitting data over a channel. The technique uses a plurality of sub-carrier frequencies (sub-carriers) within a channel bandwidth to transmit data. These sub-carriers are arranged for optimal bandwidth efficiency compared to conventional frequency division multiplexing (FDM) which can waste portions of the channel bandwidth in order to separate and isolate the sub-carrier frequency spectra and thereby avoid inter-carrier interference (ICI). By contrast, although the frequency spectra of OFDM sub-carriers overlap significantly within the OFDM channel bandwidth, OFDM nonetheless allows resolution and recovery of the information that has been modulated onto each sub-carrier.

[0005] The transmission of data through a channel via OFDM signals also provides several other advantages over more conventional transmission techniques. Some of these advantages are a tolerance to multipath delay spread and frequency selective fading, efficient spectrum usage simplified sub-channel equalization, and good interference properties.

[0006] Referring now to FIG. 1, an OFDM signal 10 is transmitted as blocks of user data 12 separated by guard intervals known as cyclic prefixes 14. A cyclic prefix 14 is a copy of a portion of an adjacent block of user data 12 and is used to reduce Inter-Symbol Interference (ISI) caused by multipath fading. More particularly, only cyclic prefixes 14, as opposed to user data 12, are effected by ISI, as is known by those skilled in the art. Thus the removal of cyclic prefixes 14 by an OFDM receiver removes the effects of ISI from the received

OFDM signal.

[0007] At the OFDM receiver a received OFDM signal 10 is digitized or sampled to convert the OFDM signal from an analog to a digital signal. Afterwards, the OFDM receiver applies Fast Fourier Transform (FFT) windows to the OFDM signal to remove the cyclic prefixes from a received OFDM signal. Ideally, an OFDM window 16 only passes user data 12 to an FFT unit 18 and discards cyclic prefixes 14. However, if there is a sampling frequency offset between the OFDM transmitter and the OFDM receiver, FFT window 16 may drift beyond the boundaries of user data 12. If this drift occurs, as shown in FIG. 2, a portion or sample 20 of cyclic prefix 14 may be passed to FFT unit 18 and a portion or sample 22 of user data 12 may be lost. As a result, the window drifting effect may result in the presence of ISI in a received OFDM signal. Furthermore, an offset of FFT window 16 will result in a phase rotation in the output of FFT unit 18. The rotation occurs because a time shift in the time domain results in a phase rotation in the frequency domain. The phase rotation may generate errors in the user data recovered by the OFDM receiver.

[0008] One way to correct for the drifting effect is to lock the frequency of the receiver's sampler or ADC to the transmitter sampling frequency using a phase-locked loop. Turning to FIG. 3, an exemplary phase-locked loop configuration 24 includes an ADC 26 that samples a received OFDM signal. An FFT window unit 28 receives the OFDM samples, removes cyclic prefixes, and passes user data to a FFT unit 30, as discussed above. A pilot extractor 32 extracts pilots imbedded in the user data and passes the pilots to a phase difference calculator 32. A pilot is a reference signal (having a known phase) that is embedded in an OFDM symbol on a predetermined subcarrier. Phase difference calculator 32 calculates the phase difference between the pilots within the OFDM symbols and passes the calculated difference to a sampling offset detector 36. Sampling offset detector 36 detects a sampling offset between the transmitter and receiver using the calculated difference and outputs the sampling offset to a digital phase-locked loop 38. Digital phase-locked loop 38 controls the sampling clocks of ADC 26 and ensures consistent FFT window positioning throughout the reception of the transmission once digital phase-locked loop 38 has locked.

[0009] Although PLL configuration 24 ensures consistent FFT window positioning once digital phase-locked loop 38 has locked, PLL configuration 24 has several drawbacks. One drawback is that PLL configuration 24 may not correctly position the FFT window due to noise and channel effects. The incorrect positioning (i.e., window offset) may cause a phase rotation in the output of FFT unit 30 that, in turn, may cause errors in the user data recovered by the OFDM receiver. Another drawback is that digital phase-locked loop 38 of PLL configuration 24 is costly to implement.

[0010] If the local sampling clock of the OFDM receiver has a small offset with respect to the transmitter sam-

pling frequency it may be advantageous (e.g., to reduce costs) to remove the digital phase-locked loop and utilize a free-running local clock. However, by utilizing a free-running clock without a phase-locked loop, a small sampling offset, over time, can accumulate and shift the FFT window beyond the user data boundaries. As noted above, the FFT window shift may introduce errors, such as ISI, into the user data portion of a received OFDM symbol. The present invention is directed to the correction of this problem.

[0011] An Orthogonal Frequency Division Multiplexing (OFDM) receiver that extracts pilots from a fast Fourier transformed and equalized OFDM signal, and processes the extracted pilots to derive an FFT window adjustment factor and an associated equalizer tap adjustment value. The OFDM receiver simultaneously controls the position of an FFT window and the phase of equalizer taps using the FFT adjustment factor and equalizer tap adjustment value.

BRIEF DESCRIPTION OF THE DRAWINGS

[0012] The aforementioned advantages of the invention, as well as additional advantages thereof, will be more fully understood as a result of a detailed description of the preferred embodiment when taken in conjunction with the accompanying drawings in which:

FIG. 1 is a diagram of an OFDM signal having user data and cyclic prefix portions, and associated processors;

FIG. 2 is diagram illustrating the presence of FFT window drift;

FIG. 3 is a block diagram of a phase lock loop configuration for a conventional OFDM receiver;

FIG. 4 is a diagram illustrating the placement of a training sequence, user data, and pilot signals within an OFDM symbol frame according to the present invention;

FIG. 5 is a block diagram illustrating a window shift correction arrangement for an OFDM receiver according to the present invention; and

FIG. 6 is a flowchart illustrating an FFT window correction algorithm of the present invention.

[0013] The characteristics and advantages of the present invention will become more apparent from the following description, given by way of example.

[0014] Turning to FIG. 4, an exemplary OFDM symbol frame 40 of the present invention is shown. Symbol frame 40 includes a training sequence 44 containing known transmission values for each subcarrier in the OFDM carrier, and a predetermined number of cyclic prefix 42 and user data 46 pairs. User data 46 has a predetermined number of pilots 48, also containing known transmission values, embedded on predetermined subcarriers. For example, the proposed ETSI-BRAN HIPERLAN/2 (Europe) and IEEE 802.11a (USA)

wireless LAN standards, herein incorporated by reference, have four pilots located at bins or subcarriers ± 7 and ± 21 .

[0015] Referring now to FIG. 5, an FFT window synchronization network or system 50 of the present invention is shown. It should be noted that system 50 may be embodied in software, hardware, or some combination thereof. For example, system 50 may be part of an WLAN adapter that is implemented as a PC card for a notebook or palmtop computer, as a card in a desktop computer, or integrated within a hand-held computer. System 50 is coupled to a source 52 of OFDM time-domain samples (e.g., the output of an ADC driven by a free running clock that is not controlled by a PLL) that has a small sampling frequency offset with respect to the sampling frequency of an OFDM transmitter. As noted above, such an offset could cause an FFT window drift which, in turn, may result in a phase rotation in the output of an FFT unit and ISI. System 50 includes a coarse FFT window synch unit 54 coupled to source 52 and an FFT unit 56. Coarse FFT window synch unit 54 obtains an initial estimate of the FFT window position and triggers FFT unit 56 when the samples from source 52 fall within the estimated window position. Coarse window synch unit 54 may use known window synch techniques such as detection of cross-correlation peaks or autocorrelation peaks of a known training sequence (e.g., training sequence 44 of FIG. 4). Coarse window synch unit 54 obtains an approximate (within several samples of the correct window position) initial estimate of the window position. Afterwards, the window position is finely adjusted, as described in further detail below.

[0016] An equalizer 58 is coupled to the output of FFT unit 56. Equalizer 58 reduces the multi-path distortion effects of the channel that the OFDM signal is transmitted through. Equalizer 58 is initialized using a training sequence (e.g., training sequence 44 of FIG. 4) stored in a memory 60 to set the equalizer tap settings. As discussed above, the training sequence contains known transmission values on all of the subcarriers of the OFDM carrier. A conventional technique for computing an initial tap value for each subcarrier is to set the tap for the subcarrier equal to the known transmission value of the subcarrier (as stored in memory 60) divided by the output on the subcarrier received from FFT unit 56. The initialization of equalizer 58 not only reduces the effect of the channel but also cancels out a phase rotation generated by an incorrect FFT window position. However, according to a feature of the present invention, the initialization only cancels out the phase rotation of the pilot subcarriers at the time of initialization, and equalizer 58 does not track the continuous phase rotations of the pilot subcarriers caused by a drifting window position.

[0017] After the taps of equalizer 58 are initially set, equalizer 58 adapts the equalizer taps for the data subcarriers but does not adapt the taps for the pilot subcarriers (e.g., pilots 48 of FIG. 4). Equalizer 58 does not

adapt the pilot taps so the phase rotation generated by incorrect FFT window position is passed on the pilot subcarriers to a pilot extraction unit 62 and a fine FFT window synch unit 64, as discussed in further detail below.

[0018] Pilot extraction unit 62 is coupled to an output of equalizer 58 and an input of fine FFT window synch unit 64. Pilot extraction unit 62 extracts pilots (e.g., pilots 48 of FIG. 4) embedded in the user data (e.g., user data 46 of FIG. 4) sent to downstream processing 66 (e.g., demodulation, decoding, and the like) and passes the pilots to a fine FFT window synch unit 64. Fine FFT window synch unit 64 is coupled to an input of FFT unit 56 to finely adjust FFT window position. Fine FFT window synch unit 64 is also coupled to an input of equalizer 58 to adjust the phase rotation of the equalizer taps. More specifically, fine FFT window synch unit 64 executes an algorithm that provides two outputs. The first output is a fine window adjustment factor 65 that is passed to FFT unit 56 to shift an FFT window in one sample increments. The second output is a phase compensation value 67 that is passed to equalizer 58 in order to rotate the equalizer taps in an equal but opposite direction from the phase rotation that is induced by the fine adjustment of the FFT window. The rotation of the equalizer taps is necessary to avoid a discrete phase jump that could disturb the tracking ability of equalizer 58.

[0019] Referring now to FIG. 6, a flowchart 70 illustrating the algorithm of the present invention is shown. Initially, at step 72, fine FFT window synch unit 64 acquires the phase of a pilot embedded in the user data. Next, at step 74, fine FFT window synch unit 64 compares the absolute value of the acquired phase to the absolute value of Ω . Ω is defined as:

$$\Omega = kT_s + [\text{sgn}(k) * \lambda]$$

Wherein k is the subcarrier or bin location of the pilot (e.g., ± 7 or ± 21). T_s is the phase shift of the lowest positive frequency subcarrier that results from an FFT window offset of 1 sample (e.g., the phase shift that would occur at subcarrier or bin location $+1$). Thus, for k -th carrier, the corresponding phase shift is kT_s (e.g., at the 7th subcarrier the phase shift is $7T_s$). λ is a safety margin or buffer value that is added to kT_s in order to prevent a false window adjustment due to noise. The $\text{sgn}()$ denotes a signum function that generates a $+1$ or a -1 depending on the sign of the subcarrier location (e.g., at the $+7$ bin location the $\text{sgn}()$ function generates a $+1$ and at the -7 bin location the $\text{sgn}()$ function generates a -1).

[0020] If the absolute value of the phase does not exceed the absolute value of Ω , the fine FFT window synch unit 64 resets or zeroes the positive phase shift (M^+) and negative phase shift (M^-) counters and, returning to step 72, acquires the phase of the next pilot. It should be noted that there are positive and negative phase shift

counters (M^+ and M^-) for each pilot subcarrier. For example, if pilots are located at the ± 7 and ± 21 bins, there will be eight counters allocated for tracking positive and negative phase shifts on the ± 7 and ± 21 bins.

5 [0021] If the absolute value of the phase exceeds the absolute value of Ω , the fine FFT window synch unit 64, at step 78, determines if the phase shift is positive. If the phase shift is positive, the fine FFT window synch unit 64, at step 82, increments the positive phase shift counter (M^+) associated with the pilot by 1. If the phase shift is not positive, fine FFT window synch unit 64, at step 80, increments the negative phase shift counter (M^-) associated with the pilot by 1.

10 [0022] After incrementing a counter (either M^+ or M^-) for a given pilot, fine FFT window synch unit 64, at step 84, determines if a majority of the counters (M^+ or M^-) for all the pilots have reached a threshold or predetermined value. For example, if the pilots are located at ± 7 and ± 21 , fine FFT window synch unit 64 acquires the count of the eight counters (four M^+ counters and four M^- counters). Afterwards, fine FFT window synch unit 64 determines if a majority of the M^+ or M^- counters have reached a predetermined value (e.g., a value of 5). If the predetermined value has not been reached, the fine FFT window synch unit 64 returns to step 72 and acquires the phase of the next pilot. When the predetermined value is reached the FFT window is adjusted by at least 1 sample. The direction of adjustment is selected based upon which counters (either M^+ or M^-) have reached the predetermined value. Thus, if the predetermined value has been reached, fine FFT window synch unit 64, at step 86, adjust the window position of FFT 56 and the phase of the equalizer taps of equalizer 58, as discussed in further detail below. It should be noted that the threshold or predetermined value is used to reduce the effects of noise on the detection of a window offset. For example, an increase in noise may cause the absolute value of a detected pilot phase to exceed Ω once. However, only a phase rotation induced by a window shift would cause multiple successive occurrences of a pilot phase exceeding Ω .

15 [0023] Adjusting the FFT window position by a sample creates a discontinuous jump in phase for the frequency-domain data. To avoid the discontinuous jump in phase, the phase of each equalizer data tap is adjusted by kT_s radians where the direction of the rotation adjustment is opposite the direction of the rotation that would have otherwise been induced by the FFT window shift. However, it should be noted that the phases of the equalizer pilot taps are not adjusted so fine FFT window synch unit 64 can track the phase changes of the pilot subcarriers caused by FFT window drift.

20 [0024] Thus according to the principle of the present invention, an OFDM receiver extracts pilots from a Fast Fourier Transformed and equalized OFDM signal, and processes the extracted pilots to derive an FFT window adjustment factor and an associated equalizer tap adjustment value. The OFDM receiver simultaneously

controls the position of an FFT window and the phase of equalizer taps using the FFT adjustment factor and equalizer tap adjustment value.

[0025] While the present invention has been described with reference to the preferred embodiments, it is apparent that various changes may be made in the embodiments without departing from the spirit and the scope of the invention, as defined by the appended claims.

Claims

1. A method of Fast Fourier Transform (FFT) window position recovery in an Orthogonal Frequency Division Multiplexing (OFDM) receiver, the method **characterized by** the steps of:

preprocessing a received OFDM signal to produce a Fast Fourier Transformed and equalized OFDM signal;
extracting a pilot from the Fast Fourier Transformed and equalized OFDM signal;
processing the extracted pilot to derive an FFT window adjustment value and an associated equalizer tap adjustment value; and
utilizing the adjustment values in the preprocessing step.

2. The method of claim 1, **characterized in that** the OFDM receiver is implemented in a wireless LAN adapter.

3. The method of claim 1, **characterized in that** the OFDM receiver is integrated within one of a portable or desktop computer.

4. The method of claim 1, **characterized in that** the step of utilizing the adjustment values in the preprocessing step includes simultaneously controlling a position of an FFT window and a phase of an equalizer tap using the FFT window adjustment value and the associated equalizer tap adjustment value.

5. The method of claim 1, **characterized in that** the window adjustment value represents a window drift correction.

6. The method of claim 5, **characterized in that** the equalizer tap adjustment value represents a phase correction that negates the effect of the window drift correction on an equalizer tap.

7. The method of claim 6, **characterized in that** the equalizer tap is an equalizer data tap.

8. The method of claim 1, **characterized in that** the

step of processing includes:

comparing a phase of the extracted pilot to a predetermined value;
incrementing a counter if the phase exceeds the predetermined value; and
generating the FFT window adjustment value and the associated equalizer tap adjustment value if the counter exceeds a threshold value.

9. The method of claim 8, **characterized in that** the predetermined value represents a phase rotation due to an FFT window offset.

10. The method of claim 9, **characterized in that** the predetermined value also includes a noise buffer value.

11. The method of claim 8, **characterized in that** the threshold value is selected to reduce the effects of noise on FFT window position recovery.

12. An apparatus for Fast Fourier Transform (FFT) window position recovery in an Orthogonal Frequency Division Multiplexing (OFDM) receiver, the apparatus **characterized by**:

an FFT window module for receiving an OFDM signal and removing a cyclic prefix from the OFDM signal;
an FFT module for Fast Fourier Transforming the windowed OFDM signal;
an equalizer module for removing a channel distortion from the transformed OFDM signal; and
a window adjustment module for extracting a pilot from the transformed OFDM signal and controlling the operation of the FFT window module and the equalizer module in response to a phase of the extracted pilot.

13. The apparatus of claim 12, **characterized in that** the window adjustment module compares the phase of the extracted pilot with a predetermined value, and alters the operation of the FFT window module and the equalizer module if the phase of the extracted pilot exceeds the predetermined value a predetermined number of times.

14. The apparatus of claim 13, **characterized in that** the predetermined value represents a phase rotation due to an FFT window offset.

15. The apparatus of claim 14, **characterized in that** the predetermined value also includes a noise buffer value.

16. The apparatus of claim 14, **characterized in that**

the FFT window offset is an offset of one sample.

17. The apparatus of claim 12, **characterized in that**
the window adjustment module simultaneously controls a window position of the FFT window module
and a phase of an equalizer data tap of the equalizer module. 5
18. The apparatus of claim 17, **characterized in that**
the phase of the equalizer data tap is adjusted to cancel out a phase rotation induced by a shifting of
the window position. 10
19. The apparatus of claim 12, **characterized in that**
the OFDM receiver is implemented in a wireless LAN adapter. 15
20. The apparatus of claim 12, **characterized in that**
the OFDM receiver is integrated within one of a portable or desktop computer. 20

25

30

35

40

45

50

55

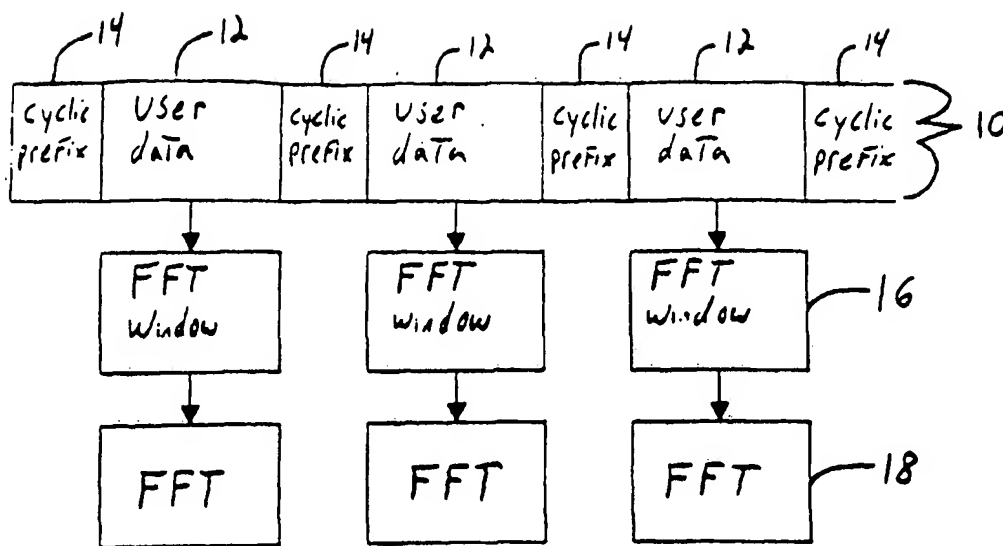


FIG. 1

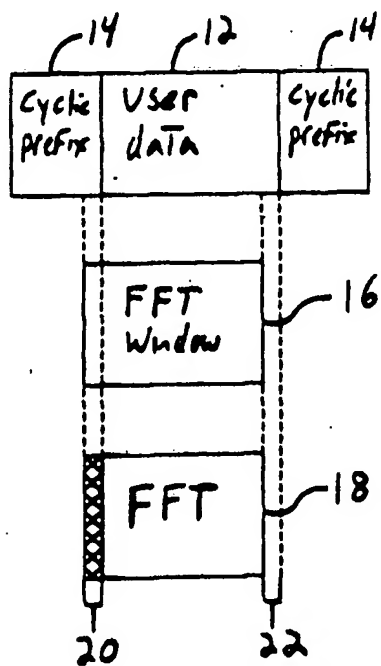


FIG. 2

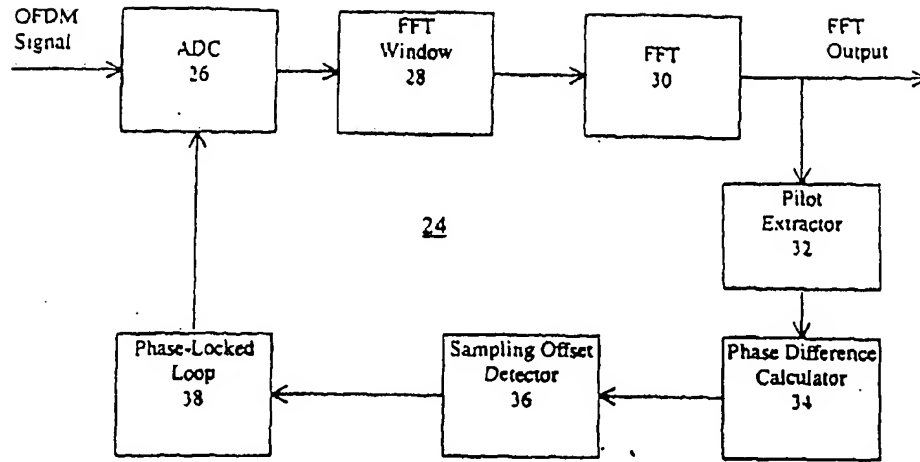


FIG. 3

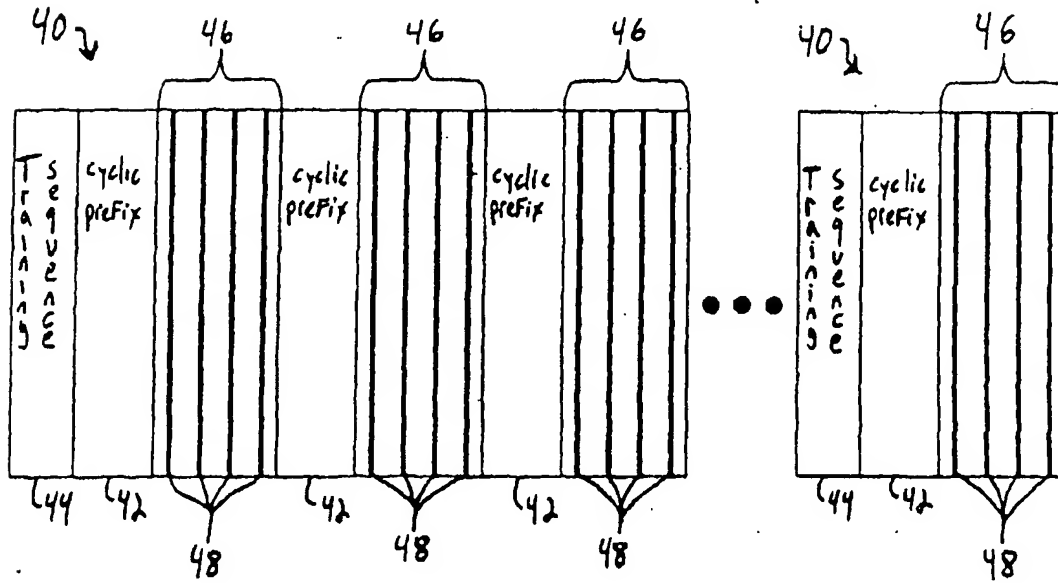


FIG. 4

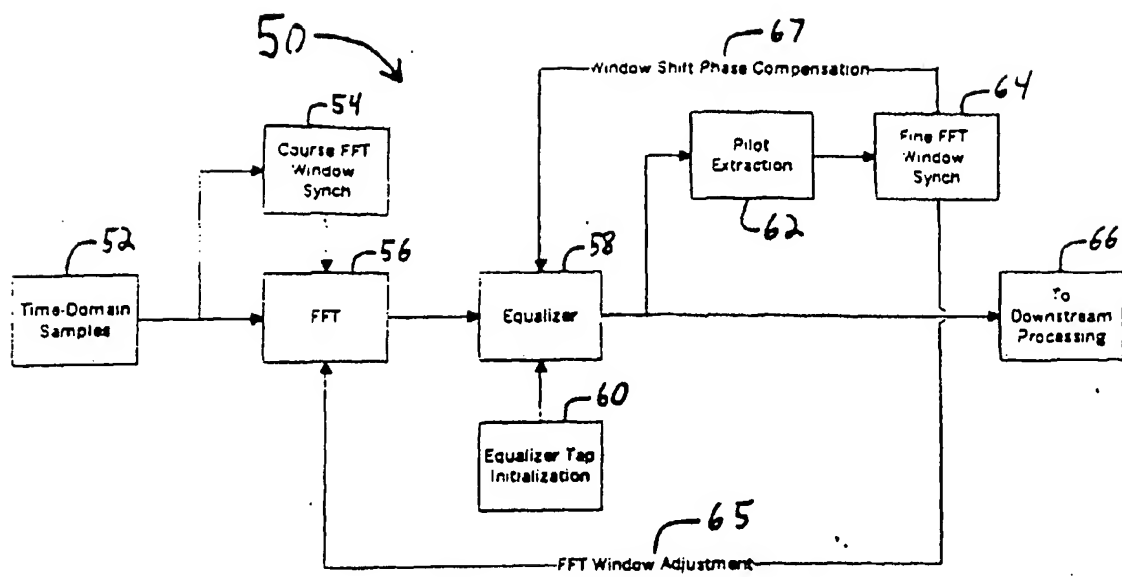


FIG. 5

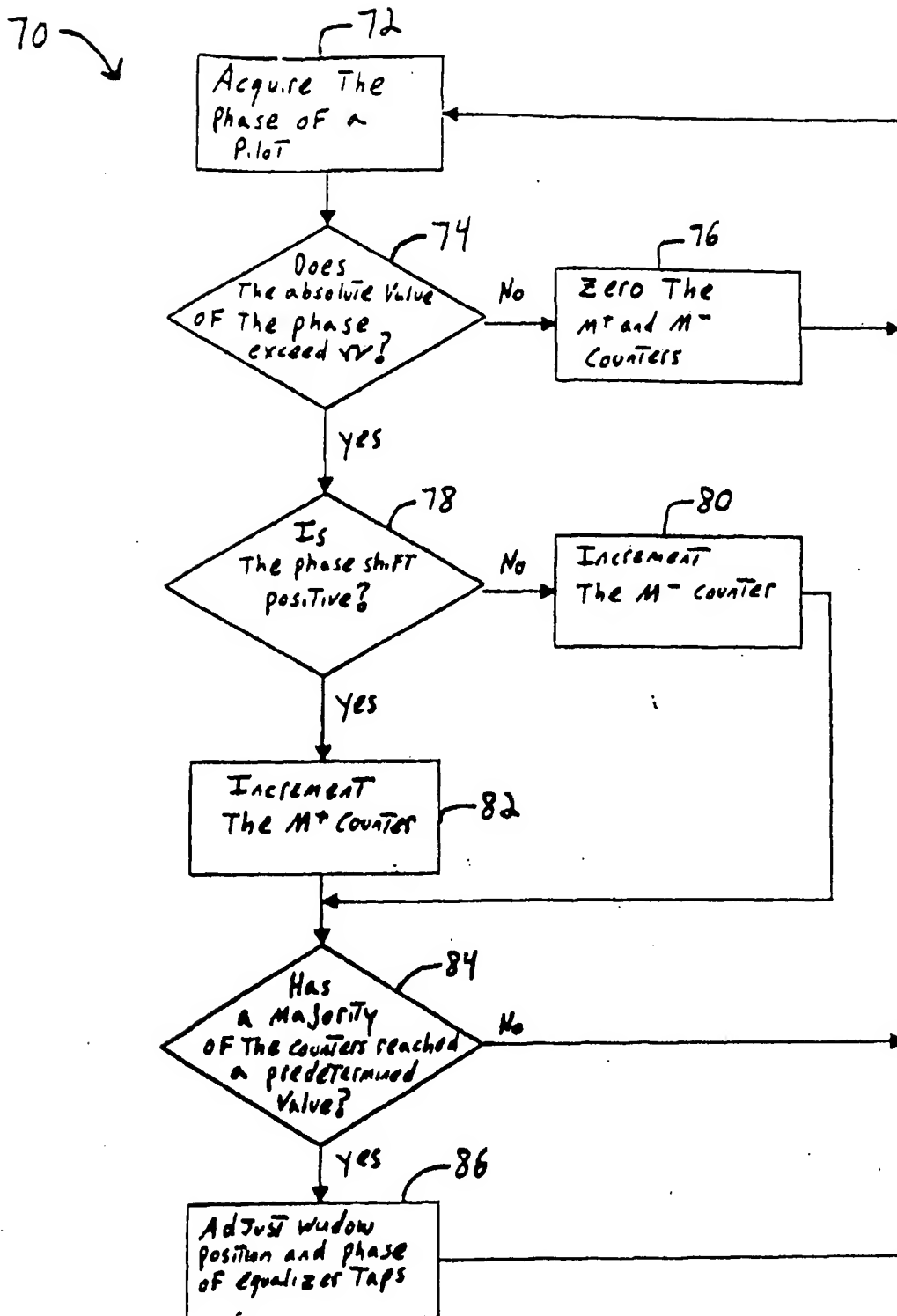


FIG. 6



European Patent
Office

EUROPEAN SEARCH REPORT

Application Number
EP 01 10 2040

DOCUMENTS CONSIDERED TO BE RELEVANT			
Category	Citation of document with indication, where appropriate, of relevant passages	Relevant to claim	CLASSIFICATION OF THE APPLICATION (Int.Cl.7)
Y	<p>CZYLWIK A: "DEGRADATION OF MULTICARRIER AND SINGLE CARRIER TRANSMISSION WITH FREQUENCY DOMAIN EQUALIZATION DUE TO PILOT-AIDED CHANNEL ESTIMATION AND FREQUENCY SYNCHRONIZATION"</p> <p>IEEE GLOBAL TELECOMMUNICATIONS CONFERENCE. PHOENIX, ARIZONA, NOV. 3 - 8, 1997, GLOBAL TELECOMMUNICATIONS CONFERENCE (GLOBECOM), NEW YORK, IEEE, US,</p> <p>vol. 1, 3 November 1997 (1997-11-03), pages 27-31, XP000737505</p> <p>ISBN: 0-7803-4199-6</p> <p>* page 27, right-hand column - page 29, left-hand column; figure 1 *</p> <p>* page 29, right-hand column, paragraph VI - page 30, left-hand column, paragraph 1 *</p>	1-20	H04L27/26 H04L25/03
Y	<p>WO 99 27671 A (CHOI HYUNG JIN ;DO SANG HYUN (KR); KIM DONG KYU (KR); SAMSUNG ELEC) 3 June 1999 (1999-06-03)</p> <p>* page 1, line 23 - page 7, line 7; figures 1,2 *</p>	1-20	TECHNICAL FIELDS SEARCHED (Int.Cl.7)
A	<p>EP 0 903 898 A (SAMSUNG ELECTRONICS CO LTD) 24 March 1999 (1999-03-24)</p> <p>* column 1, line 14 - line 20; figures 2,4 *</p> <p>* column 4, line 32 - column 7, line 49 *</p>	1-20	H04L
A	<p>FR 2 743 967 A (FRANCE TELECOM) 25 July 1997 (1997-07-25)</p> <p>* page 9, line 2 - page 16, line 10; figures 3,7 *</p>	1-20	
The present search report has been drawn up for all claims			
Place of search MUNICH		Date of completion of the search 9 August 2001	Examiner Marzenke, M
<p>CATEGORY OF CITED DOCUMENTS</p> <p>X : particularly relevant if taken alone</p> <p>Y : particularly relevant if combined with another document of the same category</p> <p>A : technological background</p> <p>O : non-written disclosure</p> <p>P : intermediate document</p> <p>T : theory or principle underlying the invention</p> <p>E : earlier patent document, but published on, or after the filing date</p> <p>D : document cited in the application</p> <p>L : document cited for other reasons</p> <p>\$: member of the same patent family, corresponding document</p>			

EP0 FORM 1503 03 82 (P04C01)



European Patent
Office

EUROPEAN SEARCH REPORT

Application Number
EP 01 10 2040

DOCUMENTS CONSIDERED TO BE RELEVANT			
Category	Citation of document with indication, where appropriate, of relevant passages	Relevant to claim	CLASSIFICATION OF THE APPLICATION (Int.Cl.7)
P, X	EP 1 063 824 A (MATSUSHITA ELECTRIC IND CO LTD) 27 December 2000 (2000-12-27) * column 1, line 7 - line 11 * * column 26, line 10 - column 29, line 51; figures 11-15 * -----	1-20	
			TECHNICAL FIELDS SEARCHED (Int.Cl.7)
The present search report has been drawn up for all claims			
Place of search MUNICH		Date of completion of the search 9 August 2001	Examiner Marzenke, M
CATEGORY OF CITED DOCUMENTS X : particularly relevant if taken alone Y : particularly relevant if combined with another document of the same category A : technological background O : non-written disclosure P : intermediate document		T : theory or principle underlying the invention E : earlier patent document, but published on, or after the filing date D : document cited in the application L : document cited for other reasons & : member of the same patent family, corresponding document	

EPO FORM 1503 03/02 (P04001)

**ANNEX TO THE EUROPEAN SEARCH REPORT
ON EUROPEAN PATENT APPLICATION NO.**

EP 01 10 2040

This annex lists the patent family members relating to the patent documents cited in the above-mentioned European search report. The members are as contained in the European Patent Office EDP file on
The European Patent Office is in no way liable for these particulars which are merely given for the purpose of information.

09-08-2001

Patent document cited in search report	Publication date	Patent family member(s)	Publication date
WO 9927671 A	03-06-1999	KR 265735 B	15-09-2000
		AU 1508299 A	15-06-1999
		GB 2346050 A	26-07-2000
EP 0903898 A	24-03-1999	KR 224863 B	15-10-1999
		CN 1209001 A	24-02-1999
		JP 11163771 A	18-06-1999
FR 2743967 A	25-07-1997	EP 0879523 A	25-11-1998
		WO 9726742 A	24-07-1997
		JP 2000503494 T	21-03-2000
EP 1063824 A	27-12-2000	CN 1278127 A	27-12-2000
		JP 2001069119 A	16-03-2001

EPO FORM P6459

For more details about this annex : see Official Journal of the European Patent Office, No. 12/82